

Low radiation level detection with room temperature InAs detector

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ABSTRACT

Photovoltaic detectors are widely used in radiometry, photometry, spectroscopy, radiation thermometry, etc. from the UV to the IR wavelength range. As the wavelength to be measured increases from the UV to IR, different semiconductors are to be used, and with increasing wavelength the band-gap and the shunt resistance of the p-n junction decreases. Typical values at room temperature are: GaP bandgap=2,25 eV shunt resistance=10¹² Ω peak responsivity=0,44 μm, Si bandgap=1,11 eV shunt resistance=10⁹-10¹⁰ Ω peak responsivity=0,85 μm, InGaAs bandgap=0,75 eV shunt resistance = 10⁸-10⁹ Ω peak responsivity=1,5 μm, Ge bandgap=0,66 eV shunt resistance = 10-100 MΩ peak responsivity=1,7 μm, InAs bandgap = 0,36 eV shunt resistance = 10-100 Ω peak responsivity=3,1 μm.

To achieve the highest linearity photovoltaic detectors are used in the zero bias mode, i.e. they are connected to a current-to-voltage (I/V) converter. Ideally, the input impedance of the I/V converter is negligible compared to the shunt impedance of the detector, consequently the whole photogenerated current flows on the feedback impedance and will produce the output voltage, otherwise a part of it flows on the shunt impedance. A current on the shunt impedance causes a deviation from the zero bias mode, it changes the electric field in the p-n junction i.e. affecting recombination rate and collection factor of the generated electron-hole pairs. In most cases, upto the InGaAs detector the input impedance of the I/V converter at low frequencies is negligible compared to that of the detector. The situation with the low band-gap InAs detector is perfectly different, the impedances even can be in the same order of magnitude. Since low level detection requires high feedback resistance, at sufficiently high feedback impedance value the output signal shall deviate from the ideal case, in the worst case the whole photogenerated current can flow on the shunt impedance generating close to zero output voltage. Further, the shunt impedance changes with temperature and the input impedance with the change of range, the latter one is linearly proportional to the value of the feedback resistor, consequently the ratio of the input impedance to shunt impedance changes affecting the linearity.

The linearity of Si, InGaAs and Ge detectors connected to an I/V converter are thoroughly discussed in the literature, contrary to InAs detector. In an earlier work¹ it has been shown that applying a bootstrap circuit to a Ge detector - depending on the frequency of the operation - will virtually increase by 3-6 decades the shunt impedance of the detector compared to the detector alone. In the present work a similar circuitry was applied to a room temperature InAs detector. It will be shown, how the bootstrap circuit channels the photogenerated current to the feedback impedance decreasing with many decades the detectable low level limit of the detector – I/V converter unit. The linearity improvement results will be shown as a function of the chopping frequency, calculated and measured values will be compared, the noise sources will be analyzed and noise measurement results will be presented.

Keywords: radiometry, photometry, detector, linearity, photovoltaic, bootstrap

INTRODUCTION

For high accuracy radiometric measurements a detector is required that together with the attached circuit provides a linear response signal over the whole radiative power range to be measured. Recently, room temperature or near room temperature InAs detectors are widely used in laser warning receivers, process control

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monitors, temperature sensors, pulsed laser monitors, infrared spectroscopy, etc.². If calibration takes place, detectors are calibrated against cryogenic radiometers at a signal level of a couple of 100 μ W, but for low level radiation measurements they are used sometimes at a signal level of 6-8 decades lower. In some applications like in high accuracy spectrophotometry where the ratio of two measurement results provide information on a physical property as of transmittance, reflectance or absorbance, the two signals can be many decades apart. Temperature measurement should provide a linear scale over a large range. Many of the applications impose huge demand on the linearity of the measurement system. In our investigations all the measurements and calculations were carried out for a room temperature InAs p-n photodiode, but our results can be used at any low shunt resistance photodiode. In this paper the linearity of low shunt resistance photovoltaic detectors (PV) together with the attached electronic circuit will be discussed, the main emphasis will be given to the interaction of the detector and the attached circuit.

A photovoltaic detector is a p-n or p-i-n junction with a depletion region having a high electric field. This field serves for the separation of the by the incident radiation generated electron-hole pairs³. Pairs can be generated either in the depletion region or in a region where a carrier can diffuse to the depletion region with some probability. Photovoltaic detectors are usually attached to current – to – voltage (I/V) converter that should provide a stable condition (zero bias) for the detector at any range. The zero bias ensures that the electric field in the depletion region will remain constant independently from the amount of the photogenerated current, consequently the probability of the collection will be the same at any circumstances. The I/V converter under proper conditions provides a virtual ground for the detector, the inherent electric field is not effected by the detection electronics. Unfortunately, the zero bias is kept only until the feedback impedance is not much larger, then that of the photodiode. For detectors of higher bandgap and shunt resistance (GaP, Si, InGaAs) this condition is met at low frequencies. The situation with low or lower bandgap detectors (Ge, InAs, InSb) is totally different. It means that, when the photodiode is irradiated a bias appears on the photodiode, it superimposes on the electric field of the depletion region influencing the separation - collection efficiency and by that a change in the dark current appears. It has been shown, that the dark current originates from injection - diffusion and generation - recombination through surface shunt, bulk leakage and tunneling at the surface that depends on the passivation of the surface in an InAs p–n junction, as well as from background radiation. The dark current and the shunt resistance of the InAs photodiode strongly depends on the temperature.⁴ Consequently, any small change in the internal impedance of the detector due to temperature change or its ratio to the measuring circuit will affect the measured signal. Leakage and tunneling can be decreased by cooling the detector, but the background radiation remains. For mobile equipment the cooling is also non practical. Even if high bandgap detectors are used at very high sensitivity measurements, that is very high feedback resistance is applied compared to the internal resistance of the detector, this degenerating phenomenon will be experienced.

CURRENT – TO – VOLTAGE (I/V) CONVERTER

A current – to – voltage (I/V) converter is shown on Fig. 1. Ph is the photodiode, OP₁ is the operational amplifier, Z_f is the feedback impedance and U_{out} is the output voltage of the I/V converter where ideally $U_{out} = I_p * Z_f$ and I_p is the photogenerated current.

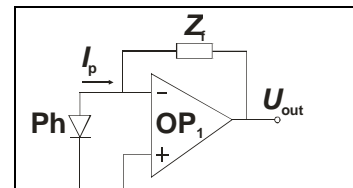


Fig. 1. Current – to – voltage converter.

The I/V converter theoretically ensures stable electrical conditions for the photodiode independently from the incident radiative power. In the depletion region collected carriers are swept out by the built in electric field to the feedback resistor. Because the I/V converter under proper conditions provides a virtual ground for the detector the inherent electric field is not effected by the detection electronics.

Fig. 2. shows a simplified equivalent circuit of the I/V converter together with the photodiode. Z_d is a nonlinear impedance representing the diode, the so called shunt impedance, Z_s is the series resistant and Z_{in} is the input impedance of the I/V converter. For high shunt resistance detectors (GaP, Si), when Z_d >> Z_s, Z_s can be neglected, but for InAs detectors they are in the same magnitude. The shunt resistance of a photovoltaic detector is defined at DC signals and zero bias and it is the first derivative of the dark current

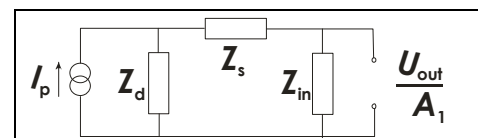


Fig. 2. Equivalent circuit of I/V converter.

with respect to the applied voltage between the poles of the detector. The shunt impedance can be defined similarly but with AC signals; consequently, it has a frequency dependency. The shunt impedance/resistance of the photodetector has a strong temperature dependency as shown in ^{4,5}. The output voltage is transformed to the input impedance of the I/V converter. The transformation factor is A₁, in the ideal case the open loop gain of the

operational amplifier. $Z_{in}=Z_f/(A_1+1)$. If $Z_{in} \ll Z_d$ then the photogenerated current flows through Z_{in} otherwise a part of it appears on Z_d and will not produce output voltage. This equivalent circuit is strictly true only for the ideal case, for non-ideal one it does not describe the physical phenomenon, but as it will be seen it provides a satisfactory model for the comprehension of the final result. If $(Z_s + Z_{in}) \ll Z_d$ then the output voltage corresponds to the whole photogenerated current. Either Z_s or Z_{in} will increase so far that the previous inequality will not hold, it will degenerate the electric field in the depletion region. On the other hand, as the frequency increases A_1 decreases i.e. both the zero bias of the photodiode and the constant electric field of the p-n junction degenerate.

Fig. 3. shows a typical curve of open-loop gain (A_1) of an operational amplifier as a function of frequency. It can be seen that at DC its value is 120dB and from a couple of Hz on it decreases with 20dB/decade. The output voltage of the I/V converter is influenced by the closed loop gain (H), too. $H=A_1*\beta$ where β is the feedback factor i.e. $\beta=(Z_d+Z_s)/(Z_d+Z_s+Z_f)$. The higher the closed loop gain is the less the amplitude error (A_{err}) of the output of the I/V converter is and vice versa. H and $1/\beta$ were marked on Fig. 3. From Fig. 3. can be seen as $1/\beta$

increases, i.e. for example Z_f increases compared to Z_d then H decreases and A_{err} increases. $A_{err} = 1/(1+H)$. Consequently, $A_{err} = (Z_d+Z_s+Z_f)/[(A_1+1)*(Z_d+Z_s) + Z_f]$. This equation shows, that either Z_d should be larger than Z_s+Z_f , or if $Z_d < (Z_s+Z_f)$, $(A_1+1)*Z_d$ should be much higher than $(A_1+1)*Z_s+Z_f$. Unfortunately, A_1 decreases with increasing frequency even in the low frequency range. Since IR photodetectors generally measure chopped radiation to exclude the effect of the background radiation and that of the dark current this worsens the situation compared to DC detection. If the temperature of a non temperature stabilized photodiode changes then Z_d and with it A_{err} changes. For example the temperature coefficient of Z_d is in the 10%/K° range for a Ge detector⁵.

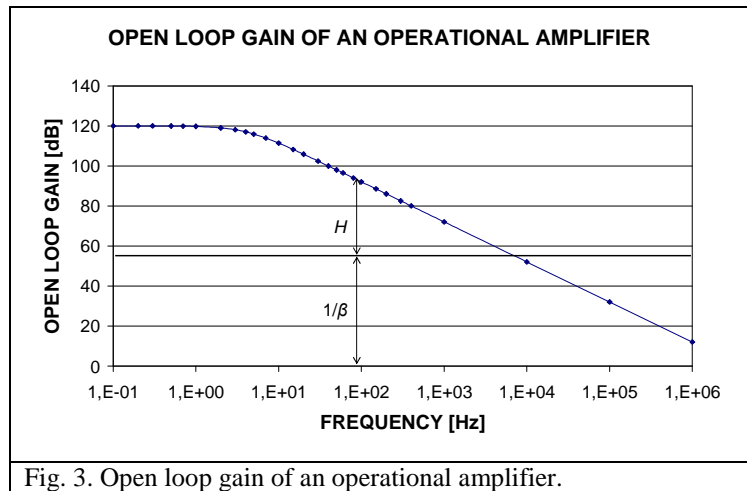


Fig. 3. Open loop gain of an operational amplifier.

BOOTSTRAPPING OF THE PHOTOVOLTAIC DETECTOR

To keep the closed loop gain at a high value or to inhibit its change can be achieved only if the impedance of the photodiode is virtually increased, it means the voltage on the photodiode is kept constant, preferably at a low level, i.e. the photodiode is bootstrapped. Fig 4. shows the schematic of the circuit. This schematic circuit was originally devised by Campastro⁶ for high frequency applications to shunt the capacitance of the diode. OP_1 is the I/V converter and OP_2 is a voltage follower. The voltage follower ensures the almost zero bias condition, on the photodiode appears only the differential input voltage of OP_2 independently from the state of OP_1 . This solution virtually increases the impedance of the photodiode by a factor of A_2 , the open loop gain of the follower⁷ producing a high closed loop gain for the I/V converter. Fig. 5. shows the equivalent circuit of the bootstrapped photodiode connected to an I/V converter. It was assumed that the offset voltage of the bootstrap amplifier was set to zero. Even if it is not zeroed, only a constant offset voltage will appear on the photodiode, i.e. the electric field in the p-n junction remains constant independently from the photocurrent and the value of the feedback impedance. As from the equivalent circuit could be seen, the equations given in the previous section should be modified, in all of them Z_d and Z_s should be replaced by A_2*Z_d and A_2*Z_s , respectively. The formula for the amplitude error then becomes $A_{err} = [A_2*(Z_d+Z_s)+Z_f]/[(A_1+1)*A_2*(Z_d+Z_s) + Z_f]$. Using a similar reasoning A_2*Z_d should be larger than $A_2*Z_s+Z_f$ or if $A_2*Z_d < (A_2*Z_s+Z_f)$ then $A_2*(A_1+1)*Z_d$ should be much higher than $A_2*(A_1+1)*Z_s+Z_f$. to get a low amplitude error. The deviation described in the previous section for the I/V converter appears only at a much higher feedback impedance value, allowing, depending on the chopping

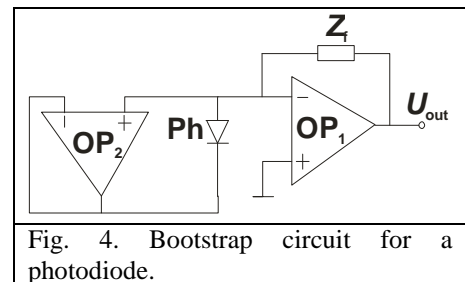


Fig. 4. Bootstrap circuit for a photodiode.

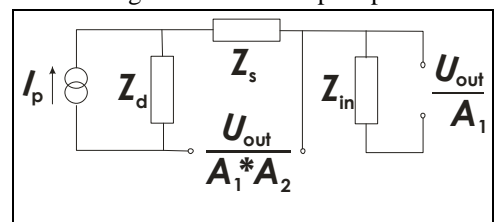


Fig. 5. Equivalent circuit of a bootstrapped photodiode connected to an I/V converter.

frequency 3 – 5 decade higher linear sensitivity. To have this system operated properly the slew rate of the voltage follower should be higher than that of the I/V converter. Contrary to the I/V converter circuit here not A_1 but the $A_1 \cdot A_2$ product should decrease to degenerate the zero bias condition.

RESULTS

To test the performance of this circuit a 2 mm diameter room temperature InAs detector was used. The feedback resistor was varied from 100 k Ω to 1 G Ω without any capacitance but the stray one and the frequency was varied from 10 Hz to 400 Hz. The configuration of the circuitry could be changed from I/V converter to bootstrapped photodiode – I/V converter by the help of a three circuit single switch. To get a stable radiation we used a tungsten halogen lamp with a monochromator, the set wavelength was 2560 nm. All of the measurements were carried out at this wavelength, except the very high sensitivity ones, where to avoid the overload a higher wavelength was used. This was taken into account by a correction factor, similarly to the range changes. The radiation was chopped by a mechanical chopper that in ideal case resulted in a time dependent irradiation on the detector and therefore in a trapezoidal photogenerated signal. Fig. 6. shows the results in logarithmic scale. It could be seen, that for the simple I/V circuitry only the 100k Ω feedback resistor provides an acceptable linearity throughout the whole frequency range, even the 1M Ω feedback causes an 11% decrease from 10 Hz to 400Hz. We usually measure at 121 Hz, to be as far as possible from the harmonics of the 50

Hz power, it means that one can use even the 1M Ω feedback at this frequency with about a 1,3% decrease. On the other hand the bootstrap circuitry provides linear measurements upto the 100 M Ω feedback, it arrives to the 1% decrease with the 1 G Ω feedback only at 25 Hz. For the clarity of the figure the curves of the 1M Ω and 100 k Ω feedbacks for the bootstrap variation were omitted, they just give a horizontal straight line over the 10 M Ω one.

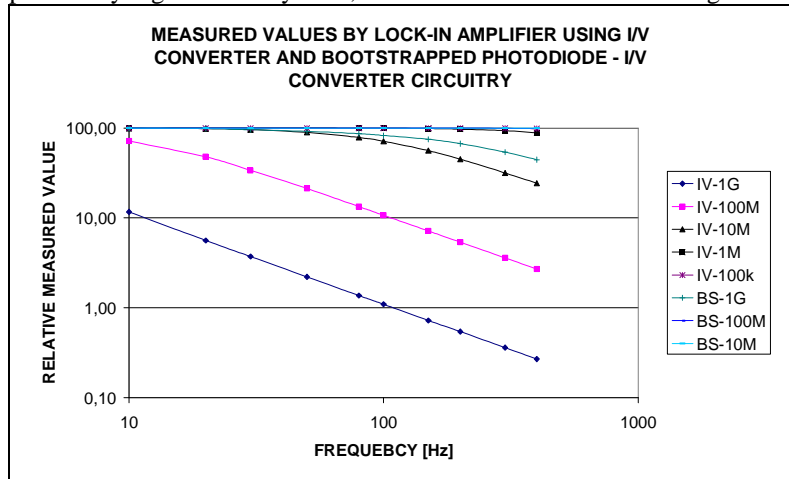


Fig. 6. Frequency dependent measured values of the same optical radiation using I/V converter and bootstrapped photodiode – I/V converter circuitry.

Fig 7. and Fig. 8. show the signal responses of the traditional I/V converter and the bootstrapped variation, respectively, taken at 30 Hz. The conditions under the curves of Fig. 7. and 8. were taken are the same. The curves were normalized and shifted to make the visualization easier. Note that from Fig. 6. it is known, that the amplitude of these curves can be decades apart. Fig 8. confirms that the bootstrap curve shows a near ideal operation of the circuit as an I/V converter, the qualitative improvement is evident. On the lowest sensitivity I/V converter curve and on all bootstrap curves higher frequency noise components are superimposed, showing the better frequency response of these cases. Essentially, this does not affect the measured values, since the lock-in amplifier measures only the amplitude of the base harmonics. For a triangular function the amplitude of the base harmonics is even lower, than for a trapezoidal function of the same amplitude, making the trapezoidal function more favorable.

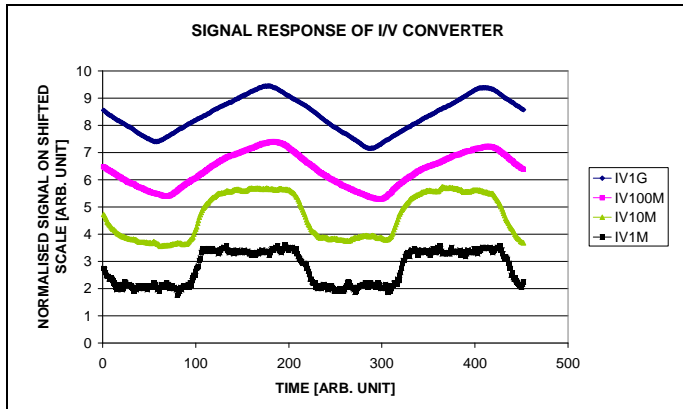


Fig. 7. Signal response of traditional I/V circuit with different feedback resistances measured at 30 Hz.

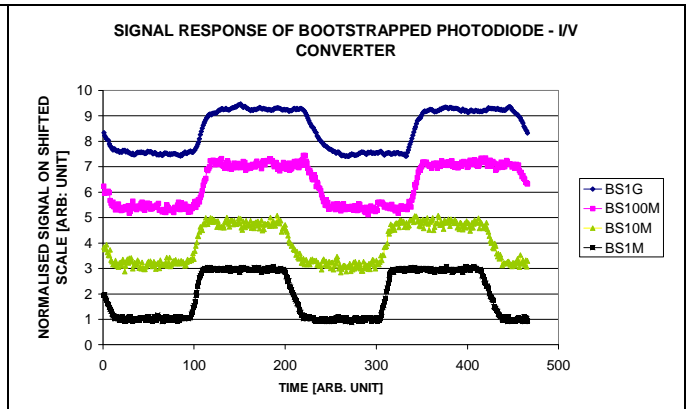


Fig. 8. Signal response of bootstrapped photodiode - I/V circuit with different feedback resistances measured at 30 Hz.

Note from Fig. 6. that the absolute value of the amplitude for the I/V converter curves are more than a decade apart.

Fig. 9. shows the change of the time response at 10 MΩ feedback resistor for the simple I/V converter as a function of the frequency. It can be seen, that from 10Hz to 400 Hz how the shape of the curves changes from trapezoidal function to triangular function and at the same time the amplitude decreases to about 1/4th of its initial value. Since the frequency of good quality mechanical choppers changes a couple of percent during a long measurement series, this results also in a change of the measured signal.

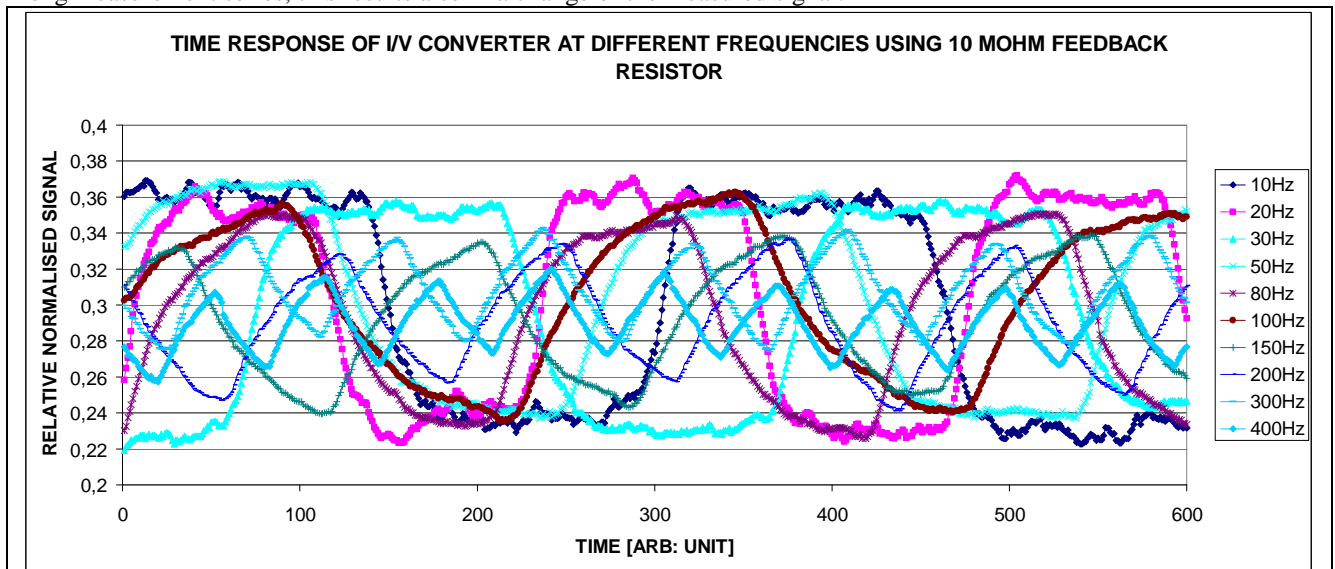


Fig. 9. Time response of I/V converter at different frequencies using 10 MΩ feedback resistance.

From the amplitude error A_{err} formulae the signal amplitude can be calculated $(1 - A_{err})$ and compared with the measured signals. Figs. 10. and 11. show these results for the simple I/V converter in logarithmic and the ratio of the measured to calculated signal in linear scale, respectively. The deviation of the calculated signal amplitude from the measured values results from the frequency dependent nonlinearity of the open-loop-gain, that also depends on the attached impedances, from the diode nonlinearity, the stray capacitances, lead inductances and measurement errors. It can be seen, that Fig. 10. provides very similar curves to sections of the open loop gain of an operational amplifier shown in Fig. 3. Since at low frequencies the feedback factor is constant, as the frequency increases the closed loop gain decreases and the amplitude error, the deviation from the ideal horizontal line, increases. For the calculations an ideal open-loop-gain was assumed. The open parameters used for a best fit solution were the shunt resistance of the photodiode, stray capacitance, and 3dB point of the open-loop-gain. It resulted in a shunt resistance about 100 Ω, that is a usual value for an InAs room temperature photodiode and the deviation between the measured and calculated values were less than ±20%. In the legends m indicates the measured and c the calculated values. Similar calculations were carried out for the bootstrapping variation, the error upto the 100 MΩ feedback resistor for all chopping frequencies was less than 1 %, for the 1 GΩ feedback it slightly exceeded the 10 %.

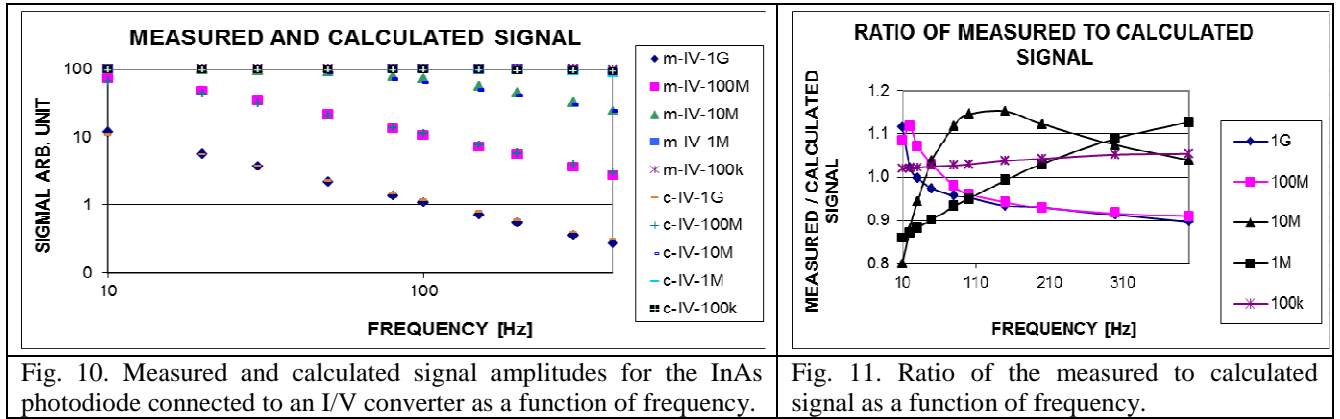


Fig. 10. Measured and calculated signal amplitudes for the InAs photodiode connected to an I/V converter as a function of frequency.

Fig. 11. Ratio of the measured to calculated signal as a function of frequency.

NOISE ANALYSES

Fig. 12. shows the noise equivalent circuit of the bootstrap variation. For simplicity only impedances are shown all of their real resistance components, if used later are referred to by the same index. The traditional current-to-voltage converter has noise sources from the detector, the amplifier and the feedback resistor. U_{1n} and U_{2n} represent the amplifier input voltage noise, I_{1n} and I_{2n} the input current noise, I_{dn} involves all the noises originating from the detector.

By applying the bootstrap the output noise originating from the feedback resistor, input noise current of the I/V converter and detector are not affected. The noise gain decreases for the input voltage noise of OP_1 because the source impedance increased but the input voltage noise of OP_2 is amplified with a gain of $Z_f/(Z_d+Z_s)$. The input current noise of OP_2 appears directly on Z_f . Summing up the spectral noise components at the output:

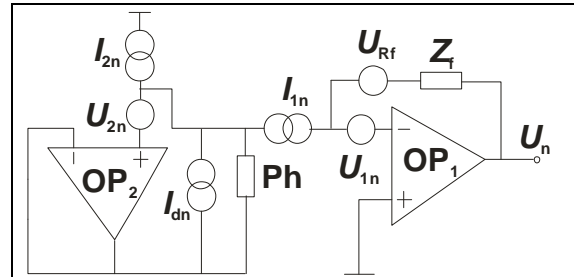


Fig. 12. Noise equivalent circuit of the bootstrapped photodiode – I/V converter circuit.

for current-to-voltage converter:

$$U_n = \sqrt{(I_{dn} \cdot Z_f)^2 + (I_{1n} \cdot Z_f)^2 + \left[\left(1 + \frac{Z_f}{Z_d + Z_s} \right) \cdot U_{1n} \right]^2 + 4kTR_f} \quad 1.)$$

for current-to-voltage converter with bootstrapped photodiode:

$$U_n = \sqrt{(I_{dn} \cdot Z_f)^2 + (I_{1n} \cdot Z_f)^2 + \left[\left(1 + \frac{Z_f}{(Z_d + Z_s)(A_2 + 1)} \right) \cdot U_{1n} \right]^2 + 4kTR_f + (I_{2n} \cdot Z_f)^2 + \left(\frac{Z_f}{Z_d + Z_s} U_{2n} \right)^2} \quad 2.)$$

If equations 1 and 2 are compared, it can be seen that bootstrapping does not result in significant increase of the noise. This has been verified experimentally by measuring the spectral noise at 121 Hz with an SR830 lock-in amplifier. Table 1. shows the results for the 100 kΩ and 1 MΩ feedback resistors. At higher feedback resistor values the noise gain for the I/V converter is already impaired by the non-ideal operation, consequently a comparison has no sense.

Feedback resistor	I/V converter	Bootstrapped photodiode I/V converter circuit
100 kΩ	45,67 μV / √Hz	57,08 μV / √Hz
1 MΩ	34,62 μV / √Hz	35,22 μV / √Hz

CONCLUSIONS

Using an I/V converter with a photodetector, the ratio of the photodetector impedance to the quotient of the feedback impedance and the open loop gain of the amplifier; i.e. the input impedance of the I/V converter, can have a dominant role in the accuracy of the measurement of photogenerated current. If the input impedance is not negligible, then high measurement errors may occur. This unfavourable situation results in a low closed loop gain. If during a measurement cycle any of the impedances change this results in a change of the low closed loop gain that causes amplitude error. Either the detector has a low internal impedance or a high impedance detector is applied in high sensitivity measurements that requires high feedback impedance, this situation may occur. The impedance of the detector can be increased virtually by a factor of the open loop gain of the operational amplifier by using the bootstrapping technique, consequently, this error will appear only at a much higher sensitivity range i.e. at much higher feedback impedance. It restores the range-to-range ratio to its designed value for some further decades of the sensitivity range. The ultimate limit is the low-pass filter formed by the high value feedback resistance together with the inevitable stray capacitance. The signal – to – noise characteristic of the bootstrapped variation essentially did not change compared to the traditional solution.

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